

# DPDK Summit China/Asia Pacific 2016 - May 18th, 2016 Agenda

| Time          | Title   | Presenter  |
|---------------|---|--|
| 08:05 - 08:35 | Sign-in   |  |
| 08:35 - 08:40 | Opening   | Intel  |
| 08:40 - 09:15 | <b>DPDK Community Update and an Introduction to the Fast Data, FD.io, Project</b> | <b>Jim St. Leger</b><br>Software Product Line Manager,<br>Intel  |
| 09:15 - 10:15 | <b>Accelerate virtio/vhost Using DPDK in NFV/Cloud Environment</b>                | <b>Huawei Xie / 谢华伟</b><br>Software Engineer,<br>Intel<br><br><b>Jianfeng Tan / 谈鉴锋</b><br>Software Engineer,<br>Intel |
| 10:15 - 11:05 | <b>Next Gen Virtual Switch</b>  | <b>Jun Xiao / 肖骏</b><br>Founder & CTO,<br>CloudNetEngine   |
| 11:05 - 11:55 | <b>mTCP: A High-Speed User-Level TCP Stack on DPDK</b>                            | <b>Dr. KyoungSoo Park</b><br>Associate Professor<br>Electrical Engineering,<br>KAIST University                        |

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|---------------|--|--|
| 11:55 - 13:00 | Lunch  |  |
| 13:00 - 13:40 | <b>DPDK: A Journey of Migration to Linux Kernel</b>                                      | <b>Lou Yang / 娄扬</b> Software Architect, TOPSEC                                |
| 13:40 - 14:15 | <b>Building High-Performance Networked Systems with Innovative Software and Hardware</b> | <b>Dr. Kai Zhang / 张凯</b> Ph.D., University of Science and Technology of China |
| 14:15 - 15:05 | <b>A Faster Load Balancer than Google Maglev, DPDK in UCloud Vortex</b>                  | <b>Xu Liang / 徐亮</b> Director, UCloud.cn                                       |
| 15:05 - 15:20 | Tea Break  |  |
| 15:20 - 15:50 | <b>A Deep Dive Into Memory Access</b>  | <b>Wang Zhihong / 王志宏</b> Software Engineer, Intel                             |
| 15:50 - 16:10 | <b>Light and NOS</b>   | <b>Dr. Dan Li</b> Associate Professor in Computer Science, Tsinghua University |
| 16:10 - 16:40 | <b>When Ceph Meets DPDK</b>  | <b>Haomai Wang / 王豪迈</b> CTO, XSKY   |